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Application No. 10/660, 686	Prepared by	J. Roldins	Tracking Number	0598869.
Examiner-GAU <u>Lee-2881</u>	Date	9-2-04	Week Date	8-2-04
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b. Applicant(s)	g. Disclaimer	I. Print Fig.	q. PTOL-85b			
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract			
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs			
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Patent and Trademark Office	SERIAL NO.	10/600,686
LIST OF DOCUMENTARY INFORMATION	APPLICANT	Joo-Hoon Lee et al.
CITED BY APPLICANT (Use several sheets if necessary)	FILING DATE	June 20, 2003
	GROUP	Unassigned

## U.S. PATENT DOCUMENTS

EXAMINE R INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	AA	5,323,476	6/21/94	Mueller et al.	385	43	07/16/93

## **FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
АВ	WO02-063347A2	8/15/02	PCT	G02B		x
AC	JP10090537	4/10/98	Japan	G02B6	122	х

## OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AD	"Very Low Insertion Loss Arrayed-Waveguide Grating with Vertically Tapered Waveguides;" A. Sugita et al., XP000968634; September 2000; pgs. 1180-1182.
AE	"A Review on Fabrication Technologies for the Monolithic Integration of Tapers with III-V Semiconductor Devices;" I. Moerman et al.; XP002146284; December 1997; pgs. 1308-1320.

## **EXAMINER:**

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